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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/731,726	12/08/2000	Kozo Shimizu	001616	1555
7.	590 03/27/2002			
ARMSTRONG, WESTERMAN, HATTORI			EXAMINER	
Suite 1000	k NAUGHTON	IP, SIKYIN		
1725 K Street, Washington, D			ART UNIT	PAPER NUMBER
······································			1742	
			DATE MAIL ED: 03/27/2002	

Please find below and/or attached an Office communication concerning this application or proceeding.

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•	Application No.	Applicant(s)	
Office Action Summary	Examiner	Group Art Unit	
—The MAILING DATE of this communication appe	ears on the cover sheet b	eneath the correspondence address-	_
Peri d for Reply	_		
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET OF THIS COMMUNICATION.	TO EXPIRE	MONTH(S) FROM THE MAILING DA	TE
 Extensions of time may be available under the provisions of 37 CFI from the mailing date of this communication. If the period for reply specified above is less than thirty (30) days, a If NO period for reply is specified above, such period shall, by defair Failure to reply within the set or extended period for reply will, by st 	reply within the statutory minimalt, expire SIX (6) MONTHS from	um of thirty (30) days will be considered timely. In the mailing date of this communication.	THS
Status			
Responsive to communication(s) filed on(2/8	00		
☐ This action is FINAL .			
☐ Since this application is in condition for allowance exce accordance with the practice under Ex parte Quayle, 19			
Disp sition of Claims			
(VClaim(s) 1 - 14		is/are pending in the application.	
Of the above claim(s) 7-14		is/are withdrawn from considerati	on.
☐ Claim(s)			
☐ Claim(s)			
☑ Claim(s) 1-6		is/are rejected.	
☐ Claim(s)		is/are rejected. is/are objected to.	on
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DETAILED ACTION

Election/Restriction

- 1. Restriction to one of the following inventions is required under 35 U.S.C. 121:
 - I. Claims 1-6 are, drawn to a semiconductor device soldering, classified in class 228.
 - II. Claims 7-12 are, drawn to a manufacturing method of a semiconductor device, classified in class 436, subclass?.
 - III. Claims 13-14 are, drawn to a Sn-Ag based solder composition, classified in class 148.
- 2. The inventions are distinct, each from the other because:

Inventions II and I are related as process of making and product made. The inventions are distinct if either or both of the following can be shown: (1) that the process as claimed can be used to make other and materially different product or (2) that the product as claimed can be made by another and materially different process (MPEP § 806.05(f)). In the instant case the product as claimed can be made by another and materially different process such as solder with Pb-Sn alloys without Ag.

3. Inventions I-II and III are related as combination and subcombination.

Inventions in this relationship are distinct if it can be shown that (1) the combination as claimed does not require the particulars of the subcombination as claimed for

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patentability, and (2) that the subcombination has utility by itself or in other combinations (MPEP § 806.05(c)). In the instant case, the combination as claimed does not require the particulars of the subcombination as claimed because the semiconductor device has a structure and other elements. The subcombination has separate utility such as solder alloy for pipes.

- 4. Because these inventions are distinct for the reasons given above and have acquired a separate status in the art as shown by their different classification, restriction for examination purposes as indicated is proper.
- 5. Because these inventions are distinct for the reasons given above and the search required for Group I is not required for Groups II or III, restriction for examination purposes as indicated is proper.
- 6. A restriction requirement may be withdrawn in view of a clear admission by the applicant that the unpatentability of any group of inventions would imply the unpatentability of the other groups of inventions. In re Lee, 199 USPQ 108 (Cmr. Pats. 1978).
- 7. During a telephone conversation with Stephen G. Adrian, Esq. on March 19, 2002 a provisional election was made without traverse to prosecute the invention of Group I, claims 1-6. Affirmation of this election must be made by applicant in replying to this Office action. Claims 7-14 are withdrawn from further consideration

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by the examiner, 37 CFR 1.142(b), as being drawn to a non-elected invention.

Claim Rejections - 35 USC § 103

- 8. The factual inquiries set forth in *Graham v. John Deere Co.*, 383 U.S. 1, 148 USPQ 459 (1966), that are applied for establishing a background for determining obviousness under 35 U.S.C. 103(a) are summarized as follows:
 - 1. Determining the scope and contents of the prior art.
 - 2. Ascertaining the differences between the prior art and the claims at issue.
 - 3. Resolving the level of ordinary skill in the pertinent art.
 - 4. Considering objective evidence present in the application indicating obviousness or nonobviousness.
- 9. This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(f) or (g) prior art under 35 U.S.C. 103(a).
- 10. Claims 1-6 are rejected under 35 U.S.C. § 103 as being unpatentable over PL 200000 / 3-476
 115725, JP 20000118, Arai et al, or USP 6160224 to Ogashiwa et al in view of JP 409260427.
- 11. The PL 115725, JP 20000118, Arai et al, or Ogashiwa et al reference(s) disclose(s) the features including the claimed semiconductor structure and Sn-Ag solder bump. The features relied upon described above can be found in the reference(s) at their abstracts. The difference between the reference(s) and the claims

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are as follows: PL 115725, JP 20000118, Arai et al, and Ogashiwa et al do not disclose the alpha ray in Sn. However, JP 409260427 in abstract and Table 1 in col. 7 disclose(s) alpha ray could be reduced from a solder bump in the same field of endeavor or the analogous metallurgical art. Therefore, it would have been obvious to one having ordinary skill in the art of the cited references at the time the invention was made to reduce alpha rays from a solder bump as taught by JP 409260427 in order to eliminate soft error inversion ratio of semiconductor device. In re Venner, 120 USPQ 193 (CCPA 1958), In re LaVerne, et al., 108 USPQ 335, and In re Aller, et al., 105 USPQ 233.

Conclusion

The above rejection relies on the reference(s) for all the teachings expressed in the text(s) of the references and/or one of ordinary skill in the metallurgical art would have reasonably understood or implied from the text(s) of the reference(s). To emphasize certain aspect(s) of the prior art, only specific portion(s) of the text(s) have been pointed out. Each reference as a whole should be reviewed in responding to the rejection, since other sections of the same reference and/or various combination of the cited references may be relied on in future rejection(s) in view of amendment(s).

All recited limitations in the instant claims have been meet by the rejections as set forth above.

Applicant is reminded that when amendment and/or revision is required, applicant should therefore specifically point out the support for any amendments made to the disclosure. See MPEP § 2163.06 (a) and 37 C.F.R. § 1.119.

Examiner Correspondence

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to S. Ip whose telephone number is (703) 308-2542. The examiner can normally be reached on Monday to Friday from 5:30 A.M. to 2:00 P.M.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Dr. Roy V. King, can be reached on (703)-308-1146.

The facsimile phone numbers are (703) 872-9310 (non-final Official Paper only), (703) 872-9311 (after-final Official Paper only), and (703) 305-7719 (Unofficial Paper only). When filing a FAX in Technology Center 1700, please indicate in the Header (upper right) "Official" for papers that are to be entered into the file, and "Unofficial" for draft documents and other communication with the PTO that are not for entry into the file of the application. This will expedite processing of your papers.

Any inquiry of a general nature or relating to the status of this application should be directed to the Group receptionist whose telephone number is (703) 308-0651.

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SIKYIN IP PRIMARY EXAMINER ART UNIT 1742

S. Ip March 20, 2002